

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT : HUBER, Louis B. *H 7/10/03*  
 SERIAL NO : 10/091,792 *10/18/03*  
 FILED : March 6, 2002  
 TITLE : METHOD FOR MANUFACTURING A POWER CHIP RESISTOR  
 Grp./A.U. : 3729  
 Examiner : PHAN, Thiem D.  
 Conf. No. : 9696  
 Docket No. : P04870US1

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## AMENDMENT AFTER FINAL

Mail Stop AF  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

*10/18/03* OFFICIAL

Sir:

In response to the Final Rejection dated November 14, 2003, kindly consider the following remarks relative to reconsideration of the present application.

## CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8(a))

I hereby certify that this correspondence is, on the date shown below, being:

## MAILING

deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to the  
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Date: December 8, 2003

## FACSIMILE

transmitted by facsimile to the Patent and Trademark Office, Art Unit 3729 at Fax No. (703) 872-9303.

*John D. Goodhue*  
 JOHN D. GOODHUE